Docket No.: MAS-FIN-101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant.

GUNTHER REISSIG

Filed

CONCURRENTLY HEREWITH

Title

METHOD FOR PROVIDING ERROR INFORMATION RELATING

TO INCONSISTENCIES IN A SYSTEM OF DIFFERENTIAL

EQUATIONS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 6,230,101 B1 (Wallis), dated May 8, 2001;

U.S. Patent No. 6,192,328 B1 (Kahlert et al.), dated February 20, 2001;

Patent Abstracts of Japan 09044473 A (Makoto et al.), dated February 14, 1997;

Patent Abstracts of Japan 01307868 A (Masato et al.), dated December 12, 1989;

PCT WO 99/48030 (Denk et al.), dated September 23, 1999;

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Chowdry, S. et al.: "Automatic Structure Analysis of Large Scale Differential Algebraic Systems", IEEE, 2001, pp. 798-803;

Unger, J. et al.: Structural Analysis of Differential-Algebraic Equation Systems-Theory and Applications", Computers chem. Engng., 1995, pp. 867-882; Reissig, G. et al.: "Differential-Algebraic Equations of Index 1 May Have an Arbitrarily High Structural Index", SIAM J. SCI. COMPUT., Vol. 21, No. 6, 2000, pp. 1987-1990;

Lawler, E.: "Combinatorial Optimization, Networks and Matroids", Dover Publications, Inc., 1976, front page, iii, and pp. 192-195;

Duff, I. S. et al.: "On Algorithms for Obtaining a Maximum Transversal", ACM Transactions on Mathematical Software, Vol. 7, No. 3, September 1981, pp. 315-330;

Pantelides, C. C.: "The Consistent Initialization of Differential-Algebraic Systems", SIAM. J. SCI. STAT. COMPUT., Vol. 9, No. 2, March 1988, pp. 213-231;

Schultz: "Programm-Dokumentation TITAN-02, Topologie-Test und Row-Swapping Programm-Modul TOPCHK" [Program Documentation TITAN-02, Topology Test and Row Swapping Program Module TOPCHK], Siemens AG ZTI DES 251, July 1988, front page, I, and pp. 1-15;

Röbenack, K. et al.: "Sukzessive Index-Reduktion elektrischer Netzwerke mit Hilfe einer minimalen Anzahl von Zusatzelementen" [Gradual Index Reduction of Electrical Networks with Support of a Minimal Amount of Additional Elements], Gens, W. (ed.), 43rd International Scientific Colloquium, Technical University of Ilmenau, Vol. 3, September 21-24, 1998, pp. 192-197;

Reißig, G.: "Extension of the Normal Tree Method", International Journal of Circuit Theory and Applications, Vol. 27, Revised Version, 1999, pp. 241-266;

Feldmann, U. et al.: "Lösbarkeitstest mit Hilfe eines gesonderten Gleichungssystems" [Solubility Test with Help of a Separated System of Equations], Infineon Technologies, MP PTS, January 13, 2000, pp. 1-4;

Christen, E. et al.: "Analog and Mixed-Signal Modeling Using the VHDL-AMS Language", 36th Design Automation Conference, VHDL-AMS Tutorial, June 21-25, 1999, pp. 1-198;

International Search Report, dated February 17, 2003.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant. As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,

For Applicant

WERNER H. STEMER REG. NO. 34,956

Date: November 26, 2003

Lerner and Greenberg, P.A. Post Office Box 2480 Hollywood, FL 33022-2480

Tel: (954) 925-1100 Fax: (954) 925-1101

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